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UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S)

Darrell G. Hill et al.

GROUP ART UNIT:

2826

APPLN. NO.:

10/007,754

EXAMINER:

Victor A. Mandala

DFFICIA

FILED:

November 15, 2001

TITLE:

METHOD OF MANUFACTURING A SEMICONDUCTOR COMPONENT

AND SEMICONDUCTOR COMPONENT THEREOF

Certificate of Transmission under 37 CFR 1.8

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office.

01 5-24-04 Dell

Signature

Pat Thomas

Printed Name of Person Signing Certificate

PRELIMINARY AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated March 25, 2004, and Examiner's comments with regard thereto, please enter the following amendments in the above-entitled application, without prejudice or disclaimer.

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DOCKET NO. SC10776P DOI

freescale somicanductor

FAX TRANSMITTAL SHEET

Preescale Semiconductor, Inc. A Motorola Subsidiary Intellectual Property Section Law Department 7700 West Parmer Lane MD: TX32/PL02

Austin, TX 78729

Telephone: (512) 996-6839 **Facsimile:** (512) 996-6854

2

Number of Pages (including this page)

Date:

May 24, 2004

To:

Victor A. Mandala - 2826

Location:

United States Patent and Trademark Office

Fax No.:

(703) 872-9306

From:

Joanna G. Chiu - 43,629

Subject:

10/007,754 - Darrell G. Hill

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MESSAGE:

Enclosed herewith, please find a Request for Continued Examination and Premilinary Amendment for filing in the below-identified application.

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED

	×	ALL TIENTS WHICH	TO ALTERIATE	A JARUSING	LUDIU.
1.	×	1 page Facsimile Cover Sheet			
2.	X	8 page Amendment			
3	Y	1 page Fee Transmitts) (in duplicate)			

Paid by Deposit Account: 503079, Freescale Semiconductor, Inc. \$00

PLEASE GIVE THESE PAPERS TO:

EXAMINER: Victor A. Mandala

GROUP ART UNIT: 2826 SERIAL NO.: 10/007,754 FILED: NOVEMBER 15, 2001 INVENTOR: DARRELL G. HILL

	-			Com	plete l	f Known			
FEE 7	Application Nur	nber	10/007,754						
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Detect form on subject to provide an include	First Named Inventor		Darrell G. Hill						
Applicant plains small calib, status, Cop 27 CER 1 27									
	Examiner Name			Victor A. Mandala					
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METHOD OF PAYMENT (check all that apply)				FF	E CAL	CIII ATION (continued)			
		FEE CALCULATION (continued) 3. ADDITIONAL FEES							
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Deposit Account Number 503079		Fee	Fee	Fee	Fee				
Deposit Account Name Freescale Semiconductor,	Inc.	Code :	(5)	Code	(3)	Fee Description			
The Director is authorized to: (check all that apply)		1051	130	2051	65	Surchargo - late filing lee or oath			
X Charge lee(s) indicated below X Credit any overp	ayments	1052	50	2052	25	Surcharge - late Provisional filing			
Charge any additional fee(s) during the pendency of this applica	alion	1053 1812	130 2520	1053 1812	190 2520	Non-English specification For filing a request for ex parte			
				•		Reexamination			
Charge fees(s) indicated below, except for the filling fee to the above-identified deposit account.	•	1804	850*	1804	920*	Requesting publication of SIR prior to Examiner action			
		1805	1840*	1805	1840*	Requesting publication of SIA after Examiner action	Œ		
FEE CALCULATION		1251	110	2251.	. 55	Extension for reply within first month			
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		1460	130	1450	130	Petitions to the Commissioner			
SUBTOTAL (1) (S)		1807	50	1807	50	Processing (se under 37 CFR 1.17(q)			
2. EXTRA CLAIM FEES	· 1	1806	180	1806	180	Submission of IOS			
Previously Extra Pec (rom Paid* Claims below	Fee Paid	8021	40	8021	40	Recording each paicht assignment per property (times number of properties)			
Total Claims - 20 = X 18 =		1809	770	2809	365	Filing a submission after final rejection (37 CFR § 1.129(a))			
Multiple Dependent 290 m	1	- 1810	770 -	2810-	- 386	For each additional invention to be examined (37 CFR § 1.129(b))			
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SUBMITTED BY				a	7	Complete (if applicable	9)		
Name (Prine/Type) Joanna G. Chiu		Registral	ion No.	43,62	29		98-6839		
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